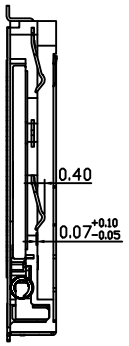
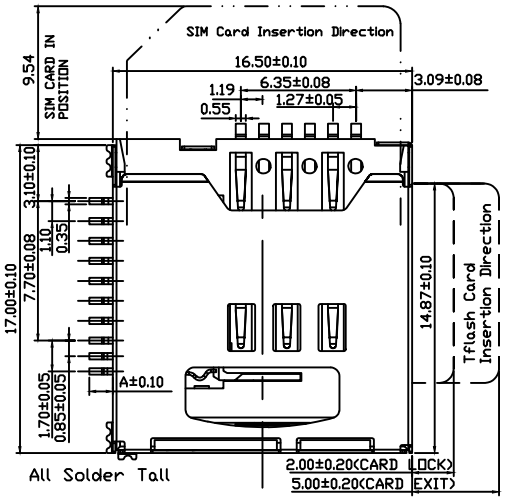
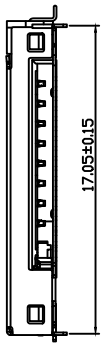
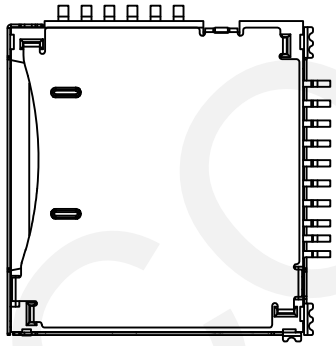
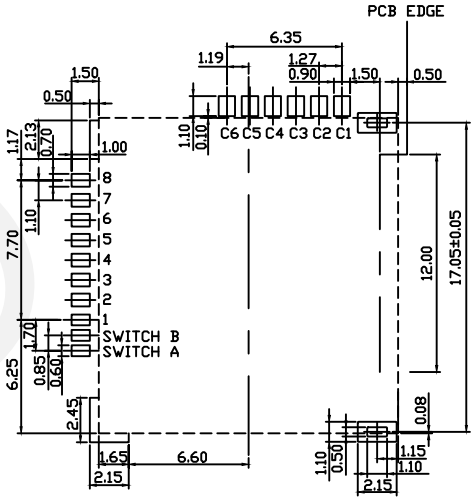




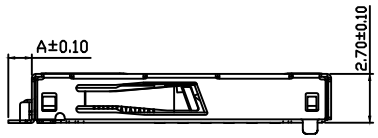


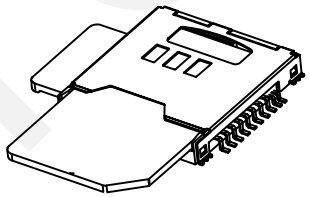




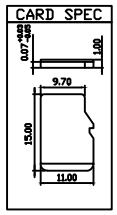
RECOMMENDED PCB LAYOUT

SECLE1:1





T-FLASH CARD				SIM CARD	
PIN NO	NAME	TYPE	DESCRIPTION	PIN NO	NAME
1	DAT2	I/O/PP	Date Line (Bit2)	C1	VCC
2	CD/DAT3	I/O/PP	Card Detect Date tine(Bit3)	C2	RST
3	CMD	PP	Commend Response	C3	CLK
4	VDD	S	Supply Voltage	C4	GND
5	CLK	I	Clock	C5	VPP
6	VSS	S	Supply Voltage Ground	C6	I/O
7	DAT0	I/O/PP	Date Line (Bit0)		
8	DAT1	I/O/PP	Date Line (Bit0)		



Notes:

Electrical:

Voltage:100V AC

Current:0.5A Max.

Contact Resistance:100mΩ Max.

Dielectric Withstanding Voltage:500V AC

Insulation Resistance:1000MΩ Min

Mechanical:

Card Insertion withdrawal Force: 13.8N Max

Push In Strength:19.6N Max.

Durability:10000 Cycles

NINGBO ERYCO ELECTRONIC CO.,LTD	UNITS:mm	SHEET SIZE: A4	SCALE:---	DRWN BY PAN	2 In 1 SIM CARD CONN,PUSH PULL 6P With Micro SD CONN,H2.7mm,SMD
	0~3	3~18	18~50	50~120	CHK'D BY FENG
	±0.12	±0.15	±0.3	±0.5	APPR BY ZHAN
					THIRD ANGLE PROJECTION
					ERYAYIM-024